

Amendment to the Claims:

The claims under examination in this application, including their current status and changes made in this paper, are respectfully presented.

42 B2
1 (currently amended). A method of downloading a program to a data processor in a system, comprising:

providing the program in an executable file together with information indicative of a condition needed for execution of the program;

retrieving the program and condition information;

determining whether a data processor in the system satisfies the condition information for the retrieved program; and

~~based on said condition information responsive to the determining step,~~
downloading the program to a the data processor ~~which~~ that satisfies said condition.

2 (currently amended). The method of Claim 1, ~~wherein said downloading step includes further comprising:~~

configuring the data processor ~~based on~~ using said condition information.

3 (currently amended). The method of Claim 1, ~~wherein said downloading step includes further comprising:~~

selecting the data processor from a plurality of data processors in the system,
~~based on~~ using said condition information.

4 (original). The method of Claim 1, wherein said providing step includes providing the program and the condition information in a COFF executable file.

5 (original). The method of Claim 1, wherein said providing step includes using a compiler/linker to combine a first file containing the condition information with a second file containing the program.

A2
6 (original). The method of Claim 1, wherein said providing step includes providing the condition information in a non-downloadable section of the executable file.

✓ Cancel claim 7.

8 (currently amended). The method of Claim 7 1, wherein said condition information includes information indicative of a data processor platform requirement of said program.

9 (currently amended). The method of Claim 7 1, wherein said condition information includes information indicative of a data processor setup parameter associated with said program.

✓ Cancel claim 10.

11 (currently amended). The method of Claim 7 1, wherein said providing step includes converting input information into said condition information which is suitable for integration with the program in the executable file.

✓ Cancel claims 12 through 19.

20 (currently amended). The method of Claim 14 1, including further comprising:
providing universally unique identifiers for uniquely identifying each of the
respective a plurality of programs and their respectively corresponding condition information;
and, said integrating step including, for each of the programs,

integrating the corresponding universally unique identifier into the executable file along with the program and the corresponding condition information.

21 (currently amended). The method of Claim 14 1, wherein said integrating providing step includes integrating said a plurality of programs and their corresponding condition information corresponding to each of the plurality of programs into a single executable file.

22 (currently amended). The method of Claim 14 21, wherein said storing step includes further comprising:

storing, in the a file storage facility, a plurality of executable files, each of which includes a program and its corresponding condition information.

23 (currently amended). A data processing apparatus, comprising:

a first data processor;

a file storage facility coupled to said first data processor, said file storage facility including an executable file containing a program and information indicative of a condition needed for execution of said program; and

wherein said first data processor including an interface for is programmed to perform a sequence of operations comprising:

obtaining said program and said condition information from said file storage facility; ~~said first data processor~~

determining whether a second data processor satisfies the condition information for the obtained program; and

responsive to said second data processor satisfying the condition information, for downloading said program to a said second data processor which satisfies said condition.

24 (currently amended). A data processing apparatus, comprising:

a first data processor;

a second data processor coupled to said first data processor; and

a file storage facility coupled to said first data processor, said file storage facility including an executable file containing a program and information indicative of a condition needed for execution of said program; and

wherein said first data processor including an interface for is programmed to perform a sequence of operations comprising:

obtaining said program and said condition information from said file storage facility; ~~said first data processor~~

determining whether said second data processor satisfies the condition information for the obtained program; and

responsive to said second data processor satisfying the condition information, ~~for downloading said obtained program to said second data processor if said second data processor satisfies said condition.~~

25 (currently amended). The system of Claim 24, wherein the first and second data processors are provided on a single integrated circuit chip;

26 (currently amended). The system of Claim 25, including further comprising:
a man/machine interface coupled to said first data processor for permitting communication between said first data processor and a user.

27 (original). The system of Claim 26, wherein said man/machine interface includes at least one of a tactile interface and a visual interface.

28 (original). The system of Claim 24, wherein said first data processor is a microprocessor and said second data processor is a digital signal processor.

29 (currently amended). The system of Claim 24, including a third data processor coupled to said first data processor;

and wherein the first data processor is also programmed to select one of the second and third data processors, using said condition information.

30 (currently amended). The system of Claim 29, wherein the first, second, and third data processors provided on a single integrated circuit chip.

Cancel claims 31 and 32.

33 (currently amended). The system of Claim 24, wherein said first data processor is operable for configuring also programmed to configure said second data processor based on using said condition information.